



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

KALNITSKY ET AL.

APPLICATION No.: 09/882,538

FILING DATE: JUNE 15, 2001

FOR: METHOD FOR FORMING A SUPER SELF-
ALIGNED HETERO-JUNCTION BIPOLAR
TRANSISTOR

EXAMINER: D. FARAHANI

GROUP ART UNIT: 2814

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Sir:

Effective immediately, please direct all further communications in the above-identified patent application to the following address:

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Respectfully submitted,
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